## **Listing of Claims**:

1. (Currently Amended) A method for fabricating a light-emitting device, which comprises the steps of:

forming at least one compound semiconductor layer based on gallium nitride and being an active layer or a part of an active layer sequence; and

setting growth parameters used during production of the compound semiconductor layer such that, at least in some cases in a vicinity of dislocations in the compound semiconductor layer, regions are produced in the compound semiconductor layer having a lower thickness than remaining regions of the compound semiconductor layer, resulting in an to build up a shielding energy barrier being built up in the regions having the lower thickness.

2. (Original) The method according to claim 1, which further comprises: providing a substrate;

forming a first coating layer formed from a compound semiconductor based on gallium nitride of a first conductivity type on the substrate;

forming the compound semiconductor layer, as a light-emitting layer, over the first coating layer; and

forming a second coating layer formed from a compound semiconductor based on gallium nitride of a second conductivity type over the light-emitting layer, a composition of the compound semiconductor layer based on gallium nitride differing from a composition of the compound semiconductor of the first and second coating layers.

- 3. (Original) The method according to claim 1, which further comprises forming the regions with the lower thickness to be less than half as thick as the remaining regions of the compound semiconductor layer.
- 4. (Original) The method according to claim 1, which further comprises forming the compound semiconductor layer from an  $In_xA1_yGa_{1-x-y}N$  compound semiconductor, where  $0 \le x \le 1$ ,  $0 \le y \le 1$  and  $x + y \le 1$ .
- 5. (Original) The method according to claim 4, which further comprises setting x = 0.
- 6. (Original) The method according to claim 2, which further comprises doping the light-emitting layer with a p-type foreign substance and/or an n-type foreign substance.
- 7. (Original) The method according to claim 2, which further comprises forming the first coating layer from a  $Ga_uA1_{1-u}N$  compound semiconductor where  $0 < u \le 1$ .
- 8. (Original) The method according to claim 2, which further comprises forming the second coating layer from a  $Ga_vA1_{1-v}N$  compound semiconductor where  $0 \le v \le 1$ .
- 9. (Original) The method according to claim 2, which further comprises forming the first coating layer, the compound semiconductor layer and the second coating layer in succession on the substrate using a metal organic chemical vapor deposition process.

- 10. (Original) The method according to claim 2, which further comprises forming a buffer layer on the substrate, and the first coating layer is then formed on the buffer layer.
- 11. (Original) The method according to claim 10, which further comprises forming the buffer layer from a  $Ga_mA1_{1-m}N$  compound semiconductor where  $0 \le m \le 1$ .
- 12. (Original) The method according to claim 2, which further comprises forming the substrate from a material selected from the group consisting of sapphire, silicon carbide, zinc oxide and gallium nitride.
- 13. (Original) The method according to claim 1, which further comprises forming the active layer sequence with a quantum film structure.
- 14. (Original) The method according to claim 13, which further comprises forming the quantum film structure to include at least one GaN quantum film.
- 15. (Original) The method according to claim 14, which further comprises forming the quantum film structure as an InGaN/GaN quantum film structure.
- 16. (Original) The method according to claim 13, which further comprises forming the quantum film structure with at least one undoped GaN quantum film.

17. (Original) The method according to claim 13, which further comprises forming the compound semiconductor layer with a GaN quantum film or with an intrinsic GaN quantum film.

Claims 18-33 (Canceled)

34. (Currently Amended) A method for fabricating a light emitting device, which comprises the steps of: The method according to claim 13, wherein

forming the at least one compound semiconductor layer includes forming the an active layer or a part of an the active layer sequence of the light emitting device, the active layer or the part of the active layer sequence contains at least one compound semiconductor layer based on gallium nitride; and

setting growth parameters used during formation of the compound semiconductor layer such that, at least in some cases in a vicinity of dislocations in the compound semiconductor layer, regions are produced in the compound semiconductor layer having a lower thickness than the remaining regions of the compound semiconductor layer.